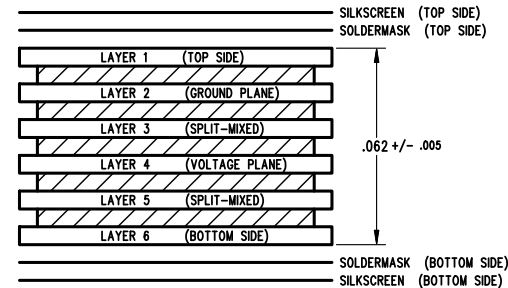


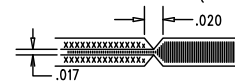
| REVISION HISTORY |     |                |           |          |
|------------------|-----|----------------|-----------|----------|
| ECO              | REV | DESCRIPTION    | APPR      | DATE     |
| -                | 1   | PRODUCTION FAB | GEORGE B. | 04-05-12 |

# LAYER STRUCTURE 6 LAYER

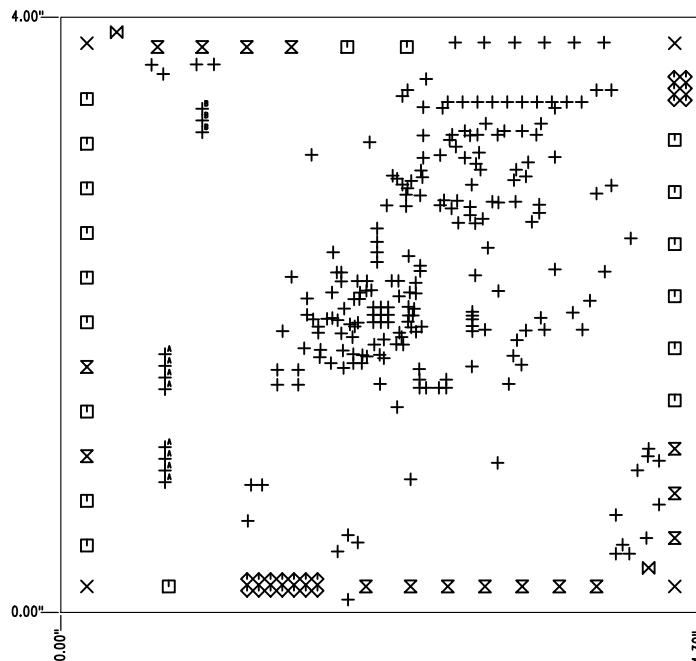


## NOTES: UNLESS OTHERWISE SPECIFIED



- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE  $0.062" \pm .005"$   
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
 $0.00"$  ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER,  $0.001"$  THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE  $\pm 0.003"$  IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



- DO NOT ALTER SOLDER MASK MAINTAIN  $.003"$  OVERSIZE ON SMT PADS. A  $.005"$  WEBBING IS REQUIRED BETWEEN SMD PADS.



| SIZE | QTY | SYM | PLATED | TOL         |
|------|-----|-----|--------|-------------|
| 10   | 215 | +   | YES    | $\pm 0.003$ |
| 190  | 4   | X   | NO     | $\pm 0.003$ |
| 94   | 18  | □   | YES    | $\pm 0.003$ |
| 35   | 20  | ◇   | YES    | $\pm 0.003$ |
| 63   | 16  | X   | YES    | $\pm 0.003$ |
| 70   | 2   | X   | NO     | $\pm 0.003$ |
| 37   | 8   | +   | YES    | $\pm 0.003$ |
| 31   | 3   | +   | YES    | $\pm 0.003$ |

|  |             |                             |  |
|--|-------------|-----------------------------|--|
| UNLESS OTHERWISE SPECIFIED<br>DIMENSIONS ARE IN INCHES<br>TOLERANCES ON ANGLE $\pm 1$<br>0.XX" = $\pm 0.01$ "<br>0.XXX" = $\pm 0.005$ "<br>INTERPRET DIM AND TOL<br>PER ASME Y14.5M-1994 | APPROVALS   |                             |  <b>LINEAR<br/>TECHNOLOGY</b><br><small>1630 MCCARTHY BLVD<br/>MILPITAS, CA 95035<br/>PH: (408)432-1900<br/>www.linear.com<br/>LTC CONFIDENTIAL -<br/>FOR CUSTOMER USE ONLY</small> |
|  | PCB DES. NC | APP ENG. GEORGE B.          |  |
| THIRD ANGLE PROJECTION   |             |                             | TITLE: FABRICATION DRAWING:<br>8-OUTPUT REGULATOR WITH SEQUENCING AND $i^2C$   |
|   |             |                             | SIZE N/A IC NO. LTC3589EUJ-1/LTC3589EUJ-2 REV. 1<br>DEMO CIRCUIT 1808B-A/B   |
| DO NOT SCALE DRAWING   | SCALE: NONE | FILENAME: DC1808B-1-A/B.PCB | SHT 1 of 1   |